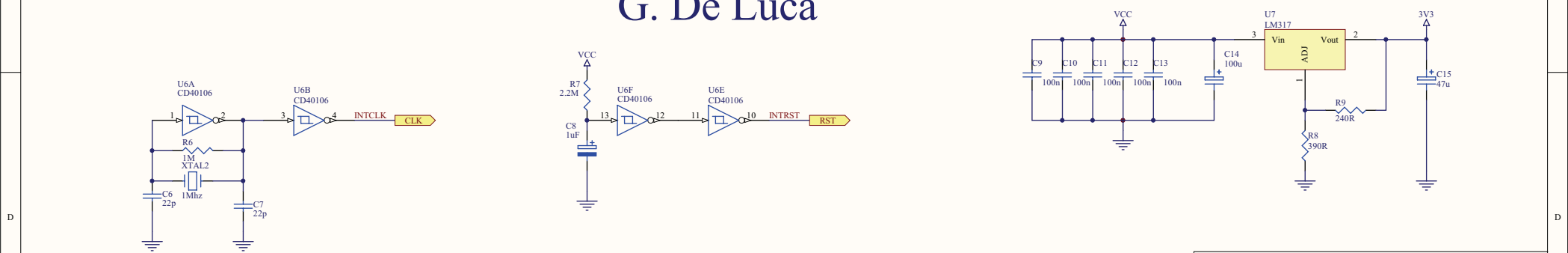


G. De Luca



Title		
Size	Number	Revision
A3		
Date:	20/06/2011	Sheet of
File:	C:\0 Miel\CPU NEW\Sch	Drawn By:

Comment	Description	Designator	Footprint	LibRef	Quantity
22p		C1, C2, C6, C7	CF21	CAP	4
1uF		C8	CAP_EL_1	ELECTRO2	1
100n		C9, C10, C11, C12, C13	CF21	CAP	5
100u		C14	CAP_EL_4	ELECTRO1	1
47u		C15	CAP_EL_4	ELECTRO1	1
CON10		J1	IDC10	CON10	1
DIN41612		J2	DIN41612_64AC_M	DIN41612_64AC_M	1
3mm		LED1, LED2	LED3MM	LED	2
10K		R1, R2, R3	RES41	RES1	3
560R		R4, R5	RES41	RES1	2
1M		R6	RES41	RES1	1
2.2M		R7		RES1	1
390R		R8	RES10	RES1	1
240R		R9	RES10	RES1	1
DIP16		SW1	DIP16	SW-DIP8	1
ATMEGA128		U1	TQFP64-1	ATMEGA128	1
DLP-USB232		U2	DIP24	DLP-USB232M-G	1
MAX485		U3, U4	DIP8	MAX485	2
CD40106		U6	DIP14	4584	1
LM317		U7	TO-220H	REGOLATORE_1	1
XPORT		XP1	XPORT	XPORT	1
14Mhz		XTAL1	XTAL2	CRYSTAL	1
1Mhz		XTAL2	XTAL2	CRYSTAL	1